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Application / Conf. No.	10/737,280 / 6162
Filing Date	December 16, 2003
First Named Inventor	Stephen M. Trimberger
Examiner Name	Esaw T. Abraham
Art Unit	2133
Patent No.	
Total Number of Pages in This Submission	Attorney Docket Number <b>X-1188 US</b>

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X-1188 US  
10/737,280

PATENT  
Conf. No. 6162



IN THE UNITED STATES PATENT OFFICE

Applicant(s): Stephen M. Trimberger  
Assignee: Xilinx, Inc.

Title: "Methods and Structures for Providing Programmable Width and Error Correction in Memory Arrays in Programmable Logic Devices"

Serial No.: 10/737,280 Filing Date: 12/16/2003

Examiner: Esaw T. Abraham Art Unit: 2133

Docket No.: X-1188 US Conf. No.: 6162

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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

In response to the Office Action mailed June 16, 2006, Applicant submits the following amendments and remarks.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 10 of this paper.